

## Lakshmi Narain College of Technology, Bhopal

### Department of Electronics & Communication Engineering

Name of faculty	Title of paper	Name of Journal	International /sci/ugc	citation
Dr. Soni Changlani	Implementation of Fruit Fly Optimization Algorithm (FFOA) to escalate the attacking efficiency of node capture attack	Computer Communications by Elsevier B.V	SCOPUS	
	Adaptive Noise Cancellation in Musical Instrument Violin signal	International Journal of Scientific & Technology Research	SCOPUS	
	A VLSI Structure for Computing of 2D-DCT Image Straining	International Journal of Science & Technology Research	SCOPUS	
	A Unique VLSI Primarily Based Structure for Computation of 2D-DCT Image Compression	Mathematical Modeling and High Performance Computing in Science and Technology	international	
	A Hybrid Technique for Digital Image Edge Detection by Combining Second Order Derivative Techniques Log and Canny	IEEE 2nd International Conference on Data, Engineering and Applications	international	
	Pipelined Architecture for JPEG Picture Straining 2D-DCT	Virtual Online National Conference-2020	OTHERS	
	Strategies and Techniques for Low Power CMOS VLSI Design	National E-Conference Innovative Research in Electronics & Tele-Communication-2020	OTHERS	
	JPEG Picture Straining 2D-DCT by Huffman Encoding	National E-Conference Innovative Research in Electronics & Tele-Communication-2020	OTHERS	
	Application of Asterisk based Automatic Dialer And IVR system employing Raspberry Pi"	International Journal of Engineering Research in Electronics and Communication Engineering	OTHERS	
	Analysis of Soft Computing with Different Approaches	Soft Computing Techniques & Communication Engineering	international	
	Low power techniques in CMOS VLSI circuits-A review of literature	National Conference on Emerging Technologies for Intelligent Electronics Systems Design	National	
	Pipelined architecture for JPEG picture Straining 2D-DCT by Huffman Encoding	International Journal of LNCT	OTHERS	
	Techniques for leakage power reduction in CMOS VLSI circuits-A review of literature	ECS (SCOPUS INDEXED JOURNAL) & conference	SCOPUS	
	Design and analysis of low power circuits for power dissipation reduction	Soft Computing Techniques & Communication Engineering	international	
	An Analysis of Grace Straining Approaches	International Journal of Engineering Research & Management Technology	OTHERS	
	2D-DCT Image Compression with Pipelined Architecture using VLSI	Journal of Information and Computational Science	UGC	
	Employing Asterisk-based IVR System Using Raspberry Pi	International Journal of All Research Education and Scientific Methods	OTHERS	
Comparative Performance Analysis of Various Digital Image Edge Detection Techniques	Journal of Xi'an Shiyou University Natural Science Edition, published in of JXSU	OTHERS		
Comparative Performance Analysis of Digital Image Noise Reduction Techniques	Journal of Information and Computational Science	UGC		

<b>Dr. G. Kumar</b>	Photonic crystal Fibres	IJNIET	UGC	23
	Review of Low-Cost VLSI Architecture of the Filter for Image Denoising	ICIETEECE-22	international	
<b>Dr. M. Zahid Alam</b>	High speed wavelet coefficient based on 2D DWT using complex multiplier	International Conference on Big Data, Cloud Computing and Intelligent Systems	International	28
	Review Paper on Discrete Wavelet Transform based on Complex Multiplier using Different Wavelet Coefficient	International Journal of LNCT	International	
	High speed Dadda unisgn and sign multiplier using optimized adder	National Conference online virtual	National	
	VLSI Design of Sign and Unsigned Irreversible and Reversible Baugh Wooley Multiplier	Journal of Emerging Technologies and Innovative Research	UGC	
	Design of efficient comparator with vlsi	International conference on soft computing tech & comm engg	International	
	Review of Polar Encoding and Decoding VLSI Architecture for Next Generation 5G-IOT Applications	International Journal of Research and Innovation in Multidisciplinary	International	
	VLSI architecture of Polar encoding and Decoding for next gen iot applications	International Journal of Emerging Technology and Innovation Research	International	
	Analysis of machine learning based LEACH robust routing in the Edge Computing systems	ELSEVIER Journal on Computer and Electrical Engineering	SCI/SCEI	
	Review of Cryptography techniques for VLSI Security applications	ICIETEECE-22	international	
<b>Dr. Monika Kapoor</b>	High performance sram circuit based on transmission gate using finfet &adiabatic logic	Jrece	International	27
	Performance Sram Circuit Based On Transmission Gate Using finfet And Adiabatic Logic	IJLNCT	international	
	Vlsi architecture for radix-4 booth complex multiplier using cyclic redundancy adder	Ijlnct	International	
	Design of low delay 4:2 compressor for multipliers	Ijermt	International	
	Design of Wimax Interleaver with Finite State Machine using VHDL	ICIETEECE-22	international	
	Blowfish Cryptography based Security Technique	International Journal of Research and Innovation in Multidisciplinary	international	
	VLSI Implementation of High Speed Data Encryption Decryption based on Blowfish Cryptography	IJRAR	international	
<b>Dr. L. N. Gahalod</b>	4 bit Error Detection and Correction using CRC in Memory Application	ICIETEECE-22	international	4
	A Review of LECTOR and STACK Technique to Reduce the Leakage Current in CMOS Circuits	ICIETEECE-22	international	
	Performance Analysis of CMOS Inverter using Sleepy Stack Power Dissipation	ICIETEECE-22	international	
	Performance Analysis of CMOS Inverter Using Sleepy Stack Power Dissipation	IRJMETS	international	
	Error Detection and Correction for Golay Code & Extended Golay Code using Cyclic Redundancy Check	IJSRSET	UGC_APPROVED	
	Performance Evaluation of Digital Image Watermarking using Level-3 DWT	International Journal of Electronics Engg.	UGC	
	Study of Different Types of Error Detection and Correction Code in Wireless Communication	IJSRSET	UGC_APPROVED	
<b>Dr.Tarun Verma</b>	Counter Measurement of Jamming in Global Navigation Satellite System and gpsreceivers	International Journal Of Scientific Progress And Research	others	9
	Next Generation Satellite Design Challenges and Solutions	International Journal of Emerging Technology and advance Engineering	others	
	Review and Design a Low powerconfigurable Adder for	International Journal of	UGC	

	CORDIC Architecture Using Carry Select Adder	Research and Analytical Review		
	Advanced Approach for CORDIC Architecture Using Carry Select Adder	International Journal of Science, Engineering and Technology	UGC	
<b>7</b> <b>Dr. Shravan Sable</b>	Performance Enhancement of Microstrip Patch Antenna Using Split Ring Resonator	International Journal of Research and Analytical Reviews	UGC	8
	Area Delay Analysis Of CMOS Reversible Gate based Add-Sub Circuit for VLSI Application	JETIR	UGC_APPROVED	
	Next Generation Satellite Design Challenges and Solutions	International Journal of Science, Engineering and Technology	other	
	Digital waveform generation using parametrized numerically controlled oscillator	International Journal of Research and Analytical Reviews	UGC	
<b>8</b> <b>Dr. Uma Shankar Kurmi</b>	Smart antennas for wireless communication	Virtual online national conference	Others	27
	Pipelined architecture for JPEG picture straining 2d-DCT by huffman	Virtual online national conference	Others	
	<u>A Study on Glaucoma Disease Detection with Image Processing Methods</u>	Advanced Engineering Forum	UGC	
	Implementation of Modified Secure hash Algorithm-3 for High speed and Throughput	International Journal of Innovative Research in Science, Engineering and Technology	Others	
	Review of GIGA Fibre Optical Communication Channel using 5G Network	Journal of Emerging Technologies and Innovative Research (JETIR)	UGC	
	<u>A Comprehensive Analysis of Identifying Lung Cancer via Different Machine Learning Approach</u>	10th International Conference on System Modeling & Advancement in Research Trends (SMART)	UGC	
<b>9</b> <b>Dr. Vijay Yadav</b>	Implementation of DVS using HDL on a Basys3 FPGA Boaed	ICIETEECE-22	international	0
	Review paper on matrix multiplication using ieee 754 floating point and different types of adders	Ijspr	OTHERS	
	Implementation of stable iir filter using adaptive genetic algorithm	Ijrar	OTHERS	
	Design and analysis of single and double precision floating point matrix multiplier using partition multiplier method	Ijcse	OTHERS	
	FPGA Implementation of Multi Error Correction and Detection for IOT Applications	JETIR	international	
	Design an efficient fast and secure modified led encryption method using vhdl	Ijlnct	OTHERS	
<b>10</b> <b>Ms. Shridha Shrivastava</b>	Design an efficient fast and secure modified led encryption method using vhdl	Ujjir	OTHERS	21
	Proposed low pdf exclusive -or gate using 130nm fet Technology	Ijsedr	open Access	
	Review on high performance Integrated ckt Transform Finite Impulse Response Module	INTERNATIONAL JOURNAL OF LNCT	National	
	VLSI Architecture of High Speed Polar Encoder and Decoder for 5th Generation Communication Application	International Journal of Innovative Research in Computer and Communication Engineering	International	
<b>11</b> <b>Dr. Prashant Chaturvedi</b>	An Efficient VLSI Filter Implementation for Image Denoising with Performance Enhancement	International Journal of Research and Innovation in Multidisciplinary	International	52
	A Novel Method for Enhancing Gain of Microstrip Antenna” , International Journal of Science & Technology Research	International Journal of Science & Technology Research	SCOPUS	
	A Unique VLSI Primarily Based Structure for Computation of 2D-DCT Image Compression	Mathematical Modeling and High Performance Computing in	OTHERS	

		Science and Technology		
	Human Area Networking using Red Tacton Technology	Mathematical Modeling and High Performance Computing in Science and Technology	OTHERS	
	Indoor Positioning System using Ultra Wide-band Technology	Mathematical Modeling and High Performance Computing in Science and Technology	OTHERS	
	A VLSI Structure for Computing of 2D-DCT Image Straining	International Journal of Science & Technology Research	SCOPUS	
	Pipelined Architecture for JPEG Picture Straining 2D-DCT	Virtual Online National Conference-2020	OTHERS	
	Low Power and High Speed Compressor based Adder using XOR-XNOR Gate	Virtual Online National Conference-2020	OTHERS	
	Real Time Location System to reliably Monitor Everyday Activities using Ultra-Wide Band Technology	Virtual Online National Conference-2020	OTHERS	
	Creating Transmission Path by using Surface of Human Body	Virtual Online National Conference-2020	OTHERS	
	Frigorifico Associated with the ESP8266	Virtual Online National Conference-2020	OTHERS	
	Iris recognition biometric security system	Virtual online national conference-2020	OTHERS	
	Smart Cleaner for Bukhari of Military Bunkers	Virtual Online National Conference-2020	OTHERS	
	A Novel Approach for Weighted Average Filter and Guided Filter based on Tunnel Image Enhancement	<u>Journal of Intelligent &amp; Fuzzy Systems.</u>	SCI	
	JPEG Picture Straining 2D-DCT by Huffman Encoding	National E-Conference Innovative Research in Electronics & Tele-Communication-2020	OTHERS	
	Minimum Path Delay for Discrete Hartley Transform using XOR-XNOR Technique	Journal of Emerging Technologies and Innovative Research	UGC	
	Analysis of Soft Computing with Different Approaches	Soft Computing Techniques & Communication Engineering	OTHERS	
	VLSI construction for 8-bit convertible Arithmetic Logic Unit based on Programmable Gate	Soft Computing Techniques & Communication Engineering	OTHERS	
	Sensor Network for Environmental Monitoring	International Journal of Engineering Research & Management Technology	OTHERS	
	Pipelined architecture for JPEG picture Straining 2D-DCT by Huffman Encoding	International Journal of LNCT	OTHERS	
	Review of Approximate Multiplier for High Speed Digital Signal Processing Application	Journal of Emerging Technologies and Innovative Research	UGC	
	Design and Performance Enhancement of 64 X 64 Approximate Multiplier for Advance VLSI-FPGA Processor Application	Journal of Information and Computational Science	UGC	
	RSS Based Selecting Technique using Master Node for WSN	Journal of Computers, Material & Continua.	SCI	
	An Analysis of Grace Straining Approaches	International Journal of Engineering Research & Management Technology	OTHERS	
	Object retrieving submarine using Node MCU	Journal of Information and Computational Science	UGC	
	2D-DCT Image Compression with Pipelined Architecture using VLSI	Journal of Information and Computational Science	UGC	
<b>12</b> <b>Mr.Arun Patel</b>	NCC-Template matching using Adaptive Statistical Fast LTP Features Makeup	IJCA	International	0
<b>13</b> <b>Dr.Rakesh Agarwal</b>	A novel method for enhancing gain of microstrip antenna	International journal of scientific & technology research, issn 2277-8616	SCOPUS	1

	An Analysis of WDM Network With Different Optimization Techniques	International Journal for Science and Advance Research in Technology	SCOPUS	
	A Survey of Minimum Wavelength Conversion Routing and Wavelength Assignment in WDM Networks	Journal of Advancement in Communication System	UGC	
<b>14</b> <b>Dr. Kamna Mishra</b>	Survey of fifth generation giga fibre optical communication	IJRDET	International	0
	Review of giga fibre optical communication channel using 5g network	IJRDET	International	
<b>15</b> <b>Mr. Ravi Bhushan</b>	Binary codes for error detection and correction	IJLNCT	International	0
	Design and implementation of extended golay code for error detection and correction	IJLNCT	International	
	Implementation of low power 17 Transistor true single phase clocking flip flop design with 45 nm CMOS technology	IRJMETS	International	
<b>16</b> <b>Mr. Sunny Jain</b>	VLSI Architecture for DWT using 5/3 Wavelet Coefficient using Vedic Math's	IJLNCT	International	0
	Efficient Time and Power of 9/7 & 5/3 Coefficient based 2-D Discrete Wavelet Transform using Vedic Math's	IJRT	International	
	Review of finite field multiplier for high speed and low delay	ICIETEECE	International	
	Vlsi implementation of roba multiplier for high speed and low delay	Ijrar	International	
<b>17</b> <b>Mr. Sourabh Pandey</b>	Video Background Subtraction Algorithm for object Tracking	IEEE Conference	IEEE	14
	Unsigned ana signed high speed Radix-4 Booth Multiplier	IJEIRSET -22	International	
	Design high speed and low power booth multiplier using cyclic redundant	Lnet journal	International	
<b>18</b> <b>Dr. Bhanu Pratap Singh</b>	Design and Analysis of GIGA Fiber like Connectivity of 5G Technology Using 60 ghz Band	Lecture Notes in Networks and Systems	SCOPUS	106
	Study of Cascode Current-Source Based Operational Transconductance Amplifier Using AMS 0.35µm Technology	National Conference on Recent Trends in Devices, Circuits and Communication	Other	
	Impact of High-K Dielectric on Charge Plasma based CPSB-JLT	Proceedings of International conference on Synthesis, Characterization & Applications of Emerging Materials with special reference to sustainable technologies	Other	
	Optical properties of ZB-inn by DFT	21st National Symposium on Radiation Physics (NSRP-21), CAD Indore	Other	
<b>19</b> <b>Mr Ankit Soni</b>	Avdr-Based Wireless Secure Key Generation with Colored Noise for iot	World Scientific.	sci	101
	Performance improvement of wireless secret key generation with colored noise for iot	Int. J. Of Communication System,	sci	
	Low Complexity Security Algorithm for CPS / iot Networks	Journal of Telecommunication, Electronic and Computer Engineering	scopus	
	A Study on Principal Component Analysis over Wireless Channel	Journal of Telecommunication, Electronic and Computer Engineering	scopus	
	Dimensionality Reduction in Wireless Physical Layer Key Generation	IEEE INDICON,	others	
<b>20</b> <b>Dr. Anuj Garg</b>	"MATLAB Model of Perturb and Observe Technique For Maximum Power Point Tracking"	Austria No.7, volume 23, Jul 2016	SCI/SCEI	0
	"A Hybrid MPPT Using Fractional Short-Circuit Current and Adoptive Stop-Size Perturb and Observe for	Communications on Applied Electronics	National	

	Fast Tracking of Max Power of High Power PV Panels”			
	“Review of Energy Harvesting Techniques for Wireless Sensor Nodes”	Communications on Applied Electronics		
	“Adaptive csma/cd: an improved mac protocol”	American journal	International	
	Design and Analysis of 2X2UWB Dumbbell Shaped microstrip patch Antenna for 5G Communication Appliances	IJRSET	International	
	Review the Technique new double-tail comparator for offset voltage optimization	UIJIR	International	
	A Low-Power High-Speed Single Ended CMOS Comparator For Flash Analog to Digital Converter.	Engineering Universe for Scientific Research and Management	International	
	Review of Cryptography Techniques for Security in Cloud Data Storage for IOT Applications	International Journal of Research and Innovation in Multidisciplinary	International	
	Hybrid Cryptography Technique for Security in Cloud Data Storage for IOT Applications	Journal of Information and Computational Science	International	
	Design and Analysis of 2 X 2 UWB Dumbbell Shaped Microstrip Patch Antenna for 5G Communication Application	International Journal of Innovative Research in Science, Engineering and Technology (IJRSET)	International	
	Review of Long-Range (Iora) Communication for 5G-IOT Applications	International Journal of Research and Innovation in Multidisciplinary	International	
	Resource Allocation using Fog-IOT NOMA Technique for 5G Long-Range Communication	Scopus Journal of Science and Technology	SCOPUS	
Dr. Bharti Gupta	BER Analysis of Cell free 5G Massive MIMO Systems With FDD and TDD Technique	IJRSET	International	
	A review of rectangular Patch Antenna with X-Slots for wireless Communications	IJETAE	International	
	An Extensive Survey on Optimal Pilot Design For channel Estimation in single /Multicarrier block Transmission systems	IJERT	International	
	Review Paper on Filter Optimization 5G Technologies for FBMC Transceiver for High Power Amplifiers	IJERT	International	32
22. Dr. Prashant Purohit	A Novel Design of Hybrid Open Slot Antenna with Parasitic Element for Wideband Applications	PIER	International	15
	An investigation of stepped open slot antenna with circular tuning stub	PIER	International	
	Performance Analysis of Associate Radix-2, Radix-4 and Radix-8 Based SDF FFT	ISMAL-CVB 2020	International	
	Implementation of Low-Power 18-Transistor True Single-Phase Clocking Flip-Flop Designs with Improved Performance	IJRCCE	International	
	Implementation of Low-Power 18-Transistor True Single-Phase Clocking Flip-Flop Designs with Improved Performance	IJRT	International	